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Kazuhide URIU et al.FILING DATE
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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
/N.D./	AJ	8-32018	2-2-96	Japan			abstract
/N.D./	AK	2002-368565	12-20-02	Japan			abstract
/N.D./	AL	10-260954	9-29-98	Japan			abstract
/N.D./	AM	2003-16133	1-17-03	Japan			abstract
	AN						

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

/N.D./	AO	T. Watanabe et al., "Parallel-Distributed FDTD-Based Full-Wave Simulator for Large-Scale Printed Wiring Boards", 2002 IEEE Topical Meeting on Electrical Performance of Electronic Packaging, October, 2002, pages 287 to 290
	AP	
	AQ	

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